

**ARCHITECTURE FOR ADVANCED SERIAL LINK
BETWEEN TWO CARDS
ABSTRACT OF THE DISCLOSURE**

5 A global architecture for a serial link connection between two cards which must
transmit data across wired media is provided. The architecture comprises a transmitter
portion and a receiver portion. The transmitter portion includes a structure and circuitry to
take digital bits from a first bit register, such as for example, an eight-bit register or a ten-bit
register, and convert these bits into serial analog transmission to the receiver portion. The
10 receiver portion includes a structure and circuitry to sample the analog transmission of the
original digital bits and reconvert the analog serial signal of the digital bits corresponding to
the original digital bits and store them in a second bit register comparable to the data stored
in the original register from which they were selected.